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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/721,184	11/26/2003	Stephen H. Tang	INTEL-0044	6574
34610	7590	05/16/2005		
FLESHNER & KIM, LLP P.O. BOX 221200 CHANTILLY, VA 20153			EXAMINER HOANG, HUAN	
			ART UNIT	PAPER NUMBER
			2827	
DATE MAILED: 05/16/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/721,184

Applicant(s)

TANG ET AL.

Examiner

Huan Hoang

Art Unit

2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 27 and 28 is/are allowed.
- 6) ☒ Claim(s) 1-10, 29 and 30 is/are rejected.
- 7) ☒ Claim(s) 11-26 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 112603.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_.

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-6 are rejected under 35 U.S.C. 102(e) as being anticipated by Fazan et al..

Fazan et al. discloses an apparatus comprising a transistor having all the elements as recited in claims 1-6 as follows:

a gate of the transistor is configured to receive a first control signal (Word Line 28a, Fig. 7A);

a first channel interface of the transistor is configured to receive a data signal (bit line 32a, Fig. 7A);

a second channel interface of the transistor is configured to receive a second control signal (paragraph [0059], lines 6-8);

wherein the memory device is a dynamic random access memory device (paragraph [0002]);

the memory device is a dynamic random access memory device (paragraph [0039]);

***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 7-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fazan et al.

Fazan et al. discloses all the elements of claims 7-9 except for the interchange of drain and source of the transistor. However, the drain and source of the transistor are interchangeable. It would have been obvious to one having ordinary skill in the art to modify Fazan et al. by interchanging the drain and the source of the transistor since the drain and source are interchangeable in the art.

5. Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Fazan et al. in view of Ohsawa.

The only difference between claim 10 and Fanza et al. is that the purge line is coupled to the second channel interface of the transistor. However, Ohsawa discloses the purge line is one of the lines for to transmit a control signal in the memory transistor (paragraph [0016]). It would have been obvious to one having ordinary skill in the art to use a purge line to transmit a control signal in a memory cell.

6. Claims 29 and 30 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fazan et al. in view of Gomes et al.

Claims 29 and 30 differ from Fazan et al. in reciting an off-die component in communication with the processor. However, Gomes et al. discloses the use of an off-chip such as a cache memory for communicating with the processor (Fig. 6 and paragraph [0038]). It would have been obvious to one having ordinary skill in the art to modify Fazan et al. by using an off-chip cache memory to communication with the processor.

***Allowable Subject Matter***

7. Claims 27 and 28 are allowed.

8. Claims 11-26 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The prior art does not teach or suggest the method of writing data to the transistor by resetting a floating body of the transistor and then writing data to the floating body of the transistor.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Huan Hoang whose telephone number is (571) 272-1779. The examiner can normally be reached on Mon-Fri 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on (571) 272-1777. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Huan Hoang  
Primary Examiner  
Art Unit 2827

HH  
5/12/05